

2192 #
PATENTIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Nicolai Kosche et al.

Title: TECHNIQUE FOR ASSOCIATING INSTRUCTIONS WITH
EXECUTION EVENTS

Application No.: 10/050,358

Filed: January 16, 2002

Examiner: Wang, Rongfa Philip

Group Art Unit: 2192

Atty. Docket No.: 004-7047

Confirmation No.: 5734

February 7, 2006

Mail Stop Amendment
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37 C.F.R. § 1.97(c)

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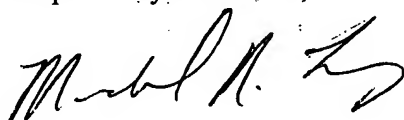
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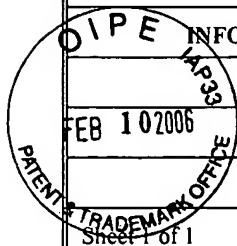
Respectfully submitted,



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PTO/SB/08A (Substitute for form 1449/PTO)		Attorney Docket No.: 004-7047
		Application No.: 10/050,358
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		First Named Inventor: Nicolai Kosche
		Filing Date: January 16, 2002
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		Examiner Name: Rongfa Philip Wang
		Date Submitted: February 7, 2006



U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
	AA	US-5,526,499	06-11-1996	Bernstein et al.		
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	AC	US-6,000,044	12-07-1999	Chrysos et al.		
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NON PATENT LITERATURE DOCUMENTS

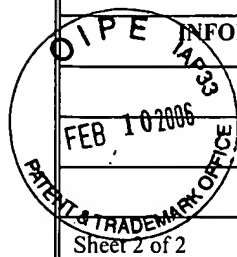
Examiner Initials*	Cite No. ¹	Include name of author (in CAPITAL LETTERS), title of article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	AA	ALPERN, B. et al., "The Jalapeno Virtual Machine," <i>IBM Systems Journal</i> , Vol. 39, No. 1, 2000, pages 211-238.	
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	AP	ITZKOWITZ, Marty et al., "Memory Profiling Using Hardware Counters," in <i>Conf. on High Performance Networking and Computing Proceedings of the 2003 ACM/IEEE Conf. on Supercomputing</i> , Phoenix, AZ, November 15-21, 2003, pages 1-12.	
Examiner Signature		Date Considered	

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Examiner Initials*	Cite No. ¹	Include name of author (in CAPITAL LETTERS), title of article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	AQ	LEBECK, Alvin R., "Cache Conscious Programming in Undergraduate Computer Science," <i>ACM SIGCSE Technical Symposium on Computer Science Education (SIGCSE '99), Proceedings of the 30th SIGCSE Tech Symposium on Comp. Sci. Edu.</i> , New Orleans, LA, USA, March 24-28, 1999, 5 pages.	
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	AS	LUK, Chi-Keung and MOWRY, Todd, "Compiler-Based Prefetching for Recursive Data Structures," <i>APLOS-VII, Proceedings of the Seventh International Conf. on Architectural Support for Programming Languages and Operating Systems</i> , Cambridge, MA, Oct. 1-5, 1996, 12 pages.	
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	AU	MOWRY, Todd C. et al., "Design and Evaluation of a Compiler Algorithm for Prefetching," <i>ASPLOS-V, Proceedings of the Fifth International Conf. on Architectural Support for Programming Languages and Operating Systems</i> , Boston, MA, USA, Oct. 12-15, 1992, pages 62-73.	
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	AW	SPRUNT, Brinkley, "The Performance Monitoring Features of the Pentium 4 Processor," <i>IEEE Micro</i> , Vol. 22, No. 4, July 2002, pages 32-42.	
	AX	SRIDHARAN, K., "VTune: Intel's Visual Tuning Environment," <i>Proceedings of USENIX-NT Windows NT Workshop</i> , August 11, 1997, Seattle, CA, USA, retrieved from the Internet at URL http://www.usenix.org/publications/library/proceedings/usenix-nt97/presentations/sridhar.html , 24 pages.	
	AY	"Program Performance Analysis Tools," Sun Microsystems, Inc. Publication 817-0922-10, May 2003, retrieved from the Internet at URL http://docs.sun.com/source/817-0922/ , 244 pages.	
	AZ	VENNERS, Bill, "The HotSpot Virtual Machine, How HotSpot Can Improve Java Program Performance and Designs," <i>Artima Developer</i> , May 1998, 7 pages, retrieved from the Internet at URL http://www.artima.com .	
	BA	WALKER, Kip et al., "Using Interpretation for Profiling the Alpha 21264a," presented at Carnegie Mellon University, October 1999, 29 pages.	
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